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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/622,052	07/17/2003	Zhiqiang Wu	TI-29922.1	2274
23494	7590 04/20/2004		EXAMINER	
TEXAS INSTRUMENTS INCORPORATED P O BOX 655474, M/S 3999			WOJCIECHOWICZ, EDWARD JOSEPH	
	DALLAS, TX 75265		ART UNIT	PAPER NUMBER
			2815	
			DATE MAILED: 04/20/2004	

Please find below and/or attached an Office communication concerning this application or proceeding.

	A a Ti a co					
	Application No.	Applicant(s)				
Office Action Summary	10/622,052	WU ET AL				
Office Action Summary	Examiner	Art Unit				
The MAILING DATE of this communication app	E Wojciechowicz	2815				
Period for Reply	Jears on the cover sneet with the	correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPL' THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.1 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a repl - If NO period for reply is specified above, the maximum statutory period of Failure to reply within the set or extended period for reply will, by statute Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be y within the statutory minimum of thirty (30) do will apply and will expire SIX (6) MONTHS TO EXCLUSE the application to become ABANDON	ays will be considered timely. In the mailing date of this communication.				
Status						
1) Responsive to communication(s) filed on						
	action is non-final.					
	/ Manual Control of the Control of t					
closed in accordance with the practice under E						
Disposition of Claims						
4) ⊠ Claim(s) <u>26-31</u> is/are pending in the application 4a) Of the above claim(s) is/are withdray 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) <u>26-31</u> is/are rejected. 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and/or	vn from consideration.					
Application Papers						
9) The specification is objected to by the Examine	r.					
10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.						
Applicant may not request that any objection to the						
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.						
The patrior declaration is objected to by the Ex	aminer. Note the attached Office	∍ Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau * See the attached detailed Office action for a list of	have been received. have been received in Applicatity documents have been received (PCT Rule 17.2(a)).	tion No red in this National Stage				
Attachment(s)						
1) Notice of References Cited (PTO-892)	4) Interview Summary	(PTO-413)				
2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date N/A.	Paper No(s)/Mail D 5)	ate Patent Application (PTO-152)				

Application/Control Number: 10/622,052

Art Unit: 2815

DETAILED ACTION

Claim Rejections - 35 USC § 112

The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

Claims 26-31 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention. Claim 26 recites, in the last two lines, that the lightly doped diffused region comprise a varying resistance in a direction parallel to the gate structure.

However, it is not apparent that there is any portion of a lightly doped region that, in itself, actually has a varying resistance. That is, each of the lightly doped regions of the invention actually have a constant doping profile and not a varying resistance profile. On the contrary, it appears that it is the absence of a lightly doped region, in conjunction with an adjacent lightly doped region that, taken together, constitutes a combined region of varying resistance. The claim recitation, however, does not clearly describe this inventive structure.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section

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351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

Claims 26-31 are rejected under 35 U.S.C. 102(e), insofar as understood, as being anticipated by Miyagi. The reference to Miyagi appears to show the claimed structure with a transistor having a gate (101) with sidewalls and source/drain regions with lightly doped portions extending under the gate whereby, in a direction parallel to the gate, that is, along its length, there are formed regions of lightly doped source and drain which alternate with regions without such a lightly doped region. See, for example, the structure of Fig. 4 of Miyagi.

The Fig. 4 structure also shows these alternating regions formed on the source side, as well as the drain side, as claimed.

Miyagi also teaches that such transistors can be formed as part of input/ output circuitry, as shown in Fig. 5 and discussed in paragraph [0039]. Miyagi states that the inventive structure can be employed in LDD devices, and that only the input protection circuit (504) and the output transistor (505) would be made up of non LDD devices. Consequently, it follows that the rest of the circuit, such as the input-output transistor (506) would have the novel LDD structure taught by Miyagi. Therefore, this teaching would meet the limitations of claims 27, 29 and 31, that the inventive transistor is part of "input/output circuitry".

Any inquiry concerning this communication or earlier communications from the examiner should be directed to E Wojciechowicz whose telephone number is (571) 272-1739. The examiner can normally be reached on Monday through Thursday.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1739. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

E Wojciechowicz Primary Examiner Art Unit 2815

EW:: ew